

PROGETTO, REALIZZAZIONE E VERIFICA DI UN SOMMATORE IN MICROSTRISCIA PER ADAPTIVE ARRAY

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Abstract

Il progetto si propone come obiettivo quello di implementare un circuito sommatore in microstriscia che verrà utilizzato per la somma dei segnali ricevuti dagli elementi di un adaptive array. Tale sommatore deve avere le seguenti caratteristiche, legate al progetto dell'adaptive array:

- 8 ingressi e 1 uscita
- 2 GHz

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